



Ai-WB2-01N Specification

Version V1.0.1

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Document resume

Version	Date	Develop/revise content	Edition	Approve
V1.0.0	2023.2.3	First Edition	Jinran Xiao	NingGuan
V1.0.1	2023.3.16	New test data, change the Flash size and rendering diagram Jinran Xiao		NingGuan





1. Product Overview

Ai-WB2-01N is a Wi-Fi&BT module developed by Shenzhen Ai-Thinker Technology Co., LTD. The module is equipped with BL602 chip as the core processor and supports Wi-Fi 802.11b/g/n protocol and BLE 5.0 protocol. The BL602 chip has a low-power 32-bit RISC CPU, 276KB RAM, and a wealth of peripheral interfaces, including SDIO, SPI, UART, I2C, IR Remote, PWM, ADC, DAC, PIR and GPIO etc. It can be widely used in Internet of Things (IoT), mobile devices, wearable electronic devices, smart home and other fields.

Figure 1 Main chip block diagram



1.1. Characteristic

- The package is DIP-11
- Support IEEE 802.11 b/g/n protocol
- Wi-Fi Security Support WPS/WEP/WPA/WPA2 Personal/WPA3
- Support 20MHz bandwidth and Max rate is 72.2 Mbps
- Bluetooth BLE 5.0, Bluetooth Mesh
- Support Station + BLE mode、Station + SoftAP + BLE mode
- Support 32-bit RISC CPU, 276KB RAM
- Secure start-up, supports mirroring with ECC-256 signature
- Support QSPI/SPI Flash On-The-Fly AES Decryption, support AES 128 CTR mode
- Support AES 128/192/256-bit encryption engine
- Support SHA-1/224/256
- Support true Random number generator (TRNG)
- Public key Accelerator (PKA), support large number basic operations, software provides signature, verification and other application program interface
- Support SPI, UART, I2C, PWM, ADC, DAC, GPIO etc
- Integrated Wi-Fi MAC/BB/RF/PA/LNA/BT
- Support a variety of sleep modes, deep sleep current 12μA
- Universal AT instruction for quick start
- Support secondary development, integrated Windows, Linux development environment



2. Main parameters

Table 1 Description of the main parameters

Model	Ai-WB2-01N		
Package	DIP-11		
Size	17.9*15.0*3.1(±0.2)mm		
Antenna	on-board PCB antenna		
Frequency	2400 ~ 2483.5MHz		
Operating temperature	-40°C ~ 85°C		
Storage temperature	-40°C ~ 125°C, < 90%RH		
Power supply	Support voltage 2.7V ~ 3.6V, supply current ≥500mA		
Interface	UART/GPIO/ADC/DAC/PWM/I2C/SPI		
Ю	8		
UART rate	Default 115200 bps		
Security	WPS/WEP/WPA/WPA2 Personal/WPA3		
Flash	Default 2MByte,Support expansion		

2.1. Static electricity requirement

Ai-WB2-01N is an electrostatic sensitive device. Therefore, you need to take special precautions when carrying it.



Figure 2 ESD preventive measures



2.2. Electrical characteristics

Table 2 Electrical characteristics table

Parameters		Condition	Min.	Typical value	Max.	Unit
Voltage Supply		VDD	2.7	3.3	3.6	V
	VIL	-	-	-	0.3*VDDIO	V
I/O	VIH	-	0.7*VDDIO	-	-	V
	VOL	-	-	0.1*VDDIO	-	V
	VOH	-	-	0.9*VDDIO	-	V
	IMAX	-	-	-	15	mA

2.3. Wi-Fi RF Performance

Table 3 Wi-Fi RF performance table

Description		Unit						
Frequency range	24	2400 ~ 2483.5MHz						
Output Power								
Mode	Min.	Typical value	Max.	Unit				
11n Mode HT20, PA output power	-	16	-	dBm				
11g Mode, PA output power	-	17	-	dBm				
11b Mode, PA output power	- 19 -			dBm				
	Receive Sensi	tivity						
Mode	Min.	Typical value	Max.	Unit				
11b, 1 Mbps	-	-98	-	dBm				
11b, 11 Mbps	-	-90	-	dBm				
11g, 6 Mbps	-	-93	-	dBm				
11g, 54 Mbps	-	-76	-	dBm				
11n, HT20 (MCS7)	-	-73	-	dBm				



2.4. BLE RF Performance

Table 4 BLE RF performance table

Description		Unit							
Frequency range	24	2400 ~ 2483.5MHz		MHz					
Output Power									
Rate Mode	Min.	Min. Typical value		Unit					
1Mbps	-	9	15	dBm					
Re	Receive Sensitivity								
Rate Mode	Min.	Typical value	Max.	Unit					
1Mbps sensitivity@30.8%PER	-	-96	-	dBm					

2.5. Power

The following power consumption data are based on a 3.3V power supply, 25°C ambient temperature, and measured using an internal voltage regulator.

- All measurements are made at the antenna interface with a filter.
- All transmission data are based on 100% duty cycle in continuous transmission mode.

Table 5 Power consumption table

Mode	Min.	AVG	Max.	Unit
Tx 802.11b, 11Mbps, POUT=+21dBm	-	347	-	mA
Tx 802.11g, 54Mbps, POUT =+18dBm	-	264	_	mA
Tx 802.11n, MCS7, POUT =+17dBm	-	236	-	mA
Rx 802.11b,packet length 1024 byte	-	63	-	mA
Rx 802.11g,packet length 1024 byte	-	63	-	mA
Rx 802.11n,Packet length 1024 byte	-	63	-	mA
Deep-Sleep	-	12	_	μΑ



3. Appearance Dimensions



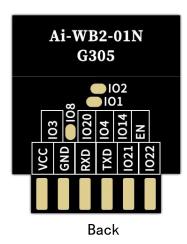


Figure 3 Appearance diagram (Rendering figure is for reference only, subject to physical objects)

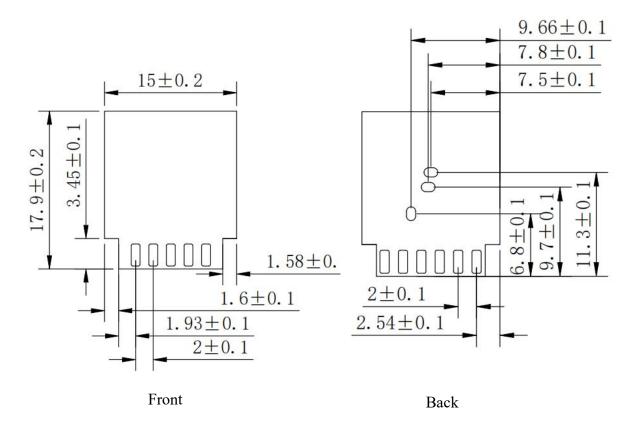


Figure 4 Dimension diagram (单位: mm)



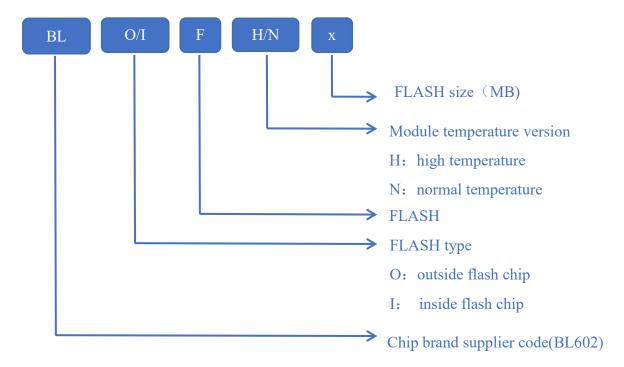


Figure 5 Shield printing information

4. Pin Definition

Ai-WB2-01N module is connected with a total of 14 pins, as shown in the pin schematic diagram, pin function definition table is the interface definition.

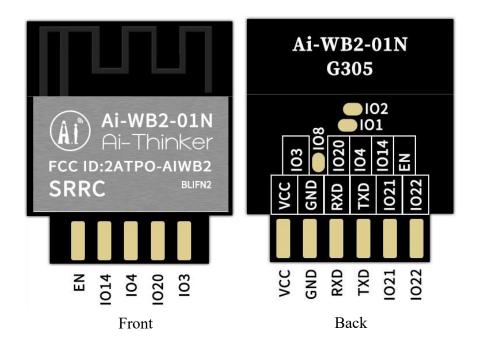


Figure 6 Schematic diagram of module pins



Table 6 Pin function definition table

No.	Na	Function
1	VC	3.3V power supply; It is recommended that the output current of the
1	С	external power supply be higher than 500mA
2	IO3	GPIO3/SPI_SCLK/IIC_SDA/PWM_CH3
3	GN	Ground
4	IO2	GPIO20/SPI_MOSI/MISO/IIC_SCL/PWM_CH0
5	RX	RXD/GPIO7/SPI_SCLK/IIC_SDA/PWM_CH2
6	IO4	GPIO4/SPI_MOSI/MISO/IIC_SCL/PWM_CH4/ADC_CH1
7	TX D	TXD/GPIO16/SPI_MOSI/MISO/IIC_SCL/PWM_CH1
8	IO1 4	GPIO14/SPI_SS/IIC_SCL/PWM_CH4/ADC_CH2/DAC_B
9	IO2	GPIO21/SPI_MOSI/MISO/IIC_SDA/PWM_CH1
10	EN	Default chip enable pin, active-high
11	IO2	GPIO22/SPI_SS/IIC_SCL/PWM_CH2
Test point	IO8	Bootstrap/GPIO8/SPI_MOSI/MISO/IIC_SCL/PWM_CH3
Test point	IO1	GPIO1/SPI_MOSI/MISO/IIC_SDA/PWM_CH1
Test point	IO2	GPIO2/SPI_SS/IIC_SCL/PWM_CH2

Note:

GPIO8 as Bootstrap, when the module enters burning mode, the power moment is high level; when the power moment is low level, the module starts normally



5. Schematic

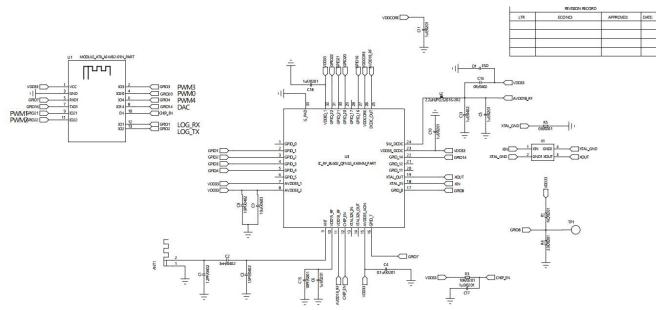


Figure 7 Module schematic



6. Antenna parameters

6.1. Schematic diagram of the antenna test prototype

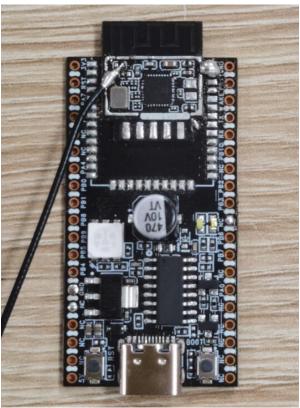


Figure 8 Schematic diagram of the antenna test prototype



6.2. Antenna S parameter

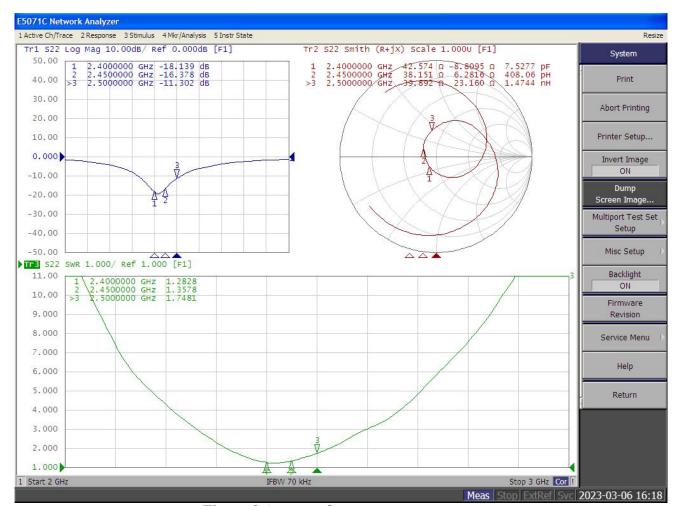


Figure 9 Antenna S parameters

6.3. Antenna Gain and Efficiency

Table 7 Antenna Gain and efficiency

Frequency ID	1	2	3	4	5	6	7	8	9	10	11
Frequency(MHz)	2400	2410	2420	2430	2440	2450	2460	2470	2480	2490	2500
Gain (dBi)	1.72	1.68	1.71	1.89	2.08	2.06	2.08	2.03	1.96	1.90	2.00
Efficiency (%)	50.25	49.60	49.19	50.19	52.19	52.11	52.58	52.16	51.67	50.86	50.88



6.4. Antenna pattern

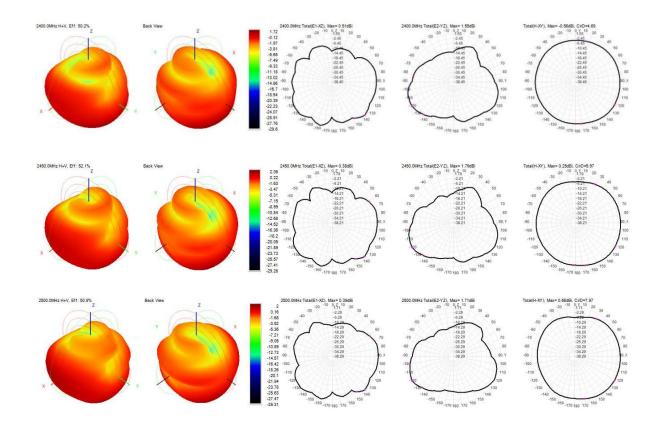


Figure 10 Antenna pattern



6.5. RF OTA parameterss

Table 8 RF OTA parameters

Item	Mode	Rate(Mbps)	Channel	Fre.(MHz)	Data(dBm)
			1	2412	16.97
	11b	11	6	2437	15.80
			13	2472	15.22
			1	2412	14.72
TRP	11g	54	6	2437	13.55
			13	2472	12.91
			1	2412	14.60
	HT20	MCS7	6	2437	13.28
			13	2472	12.49
			1	2412	-84.25
	11b	11	6	2437	-82.28
			13	2472	-82.15
			1	2412	-71.00
TIS	11g	54	6	2437	-70.57
			13	2472	-70.26
			1	2412	-66.76
	HT20	HT20 MCS7	6	2437	-64.90
			13	2472	-64.37



7. Design Guidance

7.1. Application circuit

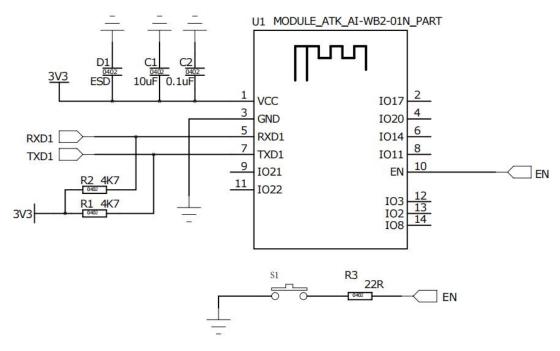


Figure 11 Application circuit diagram

- If the IO port is used as PWM, it is recommended to reserve a 4.7K pull-down resistor around the module. Especially in the application of light control, it can prevent the flashing light phenomenon at the moment of power-on start.
- The 8/NC pin, which are not available by default. If you need to use it, please contact Ai-Thinker.

7.2. Recommend PCB footprint size

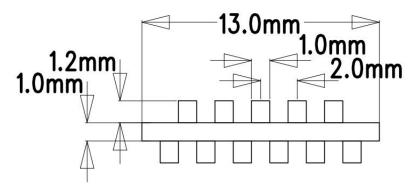


Figure 12 Recommend PCB footprint size



7.3. Antenna layout requirements

■ The following two methods are recommended for the installation position on the mainboard:

Option 1: put the module on the edge of the motherboard, and the antenna area extends out of the edge of the motherboard.

Option: put the module on the edge of the motherboard, the edge of the motherboard at the antenna position hollowed out an area.

■ In order to meet the performance of onboard antenna, it is forbidden to place metal parts around the antenna and keep away from high frequency devices.

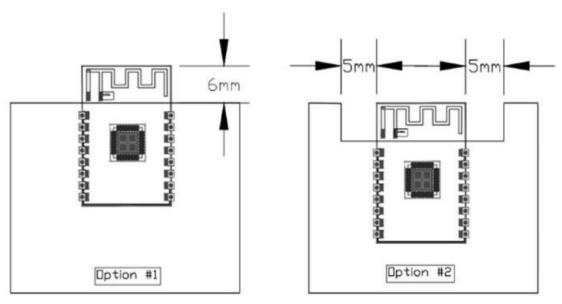


Figure 13 Antenna layout diagram

7.4. Power supply

- Recommended 3.3V voltage, peak current over 500mA.
- Power supply is recommend to use LDO; if the DC-DC is used, the ripple is recommended to be controlled within 30mV
- DC-DC power supply circuit proposes to reserve the dynamic response capacitance to optimize the output ripple with large load changes.
- It is recommended to add ESD devices to the 3.3V power interface.



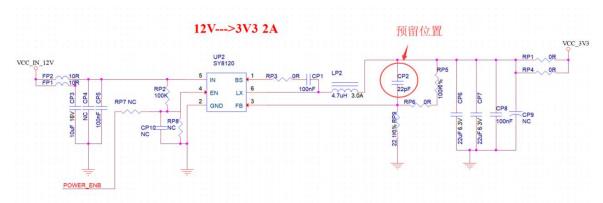


Figure 14 DC-DC step-down circuit diagram

7.5. GPIO

- There are some IO ports on the periphery of the module. If you need to use it, it is recommended to connect a 10-100 ohm resistor in series with the IO port. This inhibits overshoot and makes both sides level more stable. It is helpful for EMI and ESD.
- For special I/O ports to be pulled up and down, refer to the direction for use in the specifications, which may affect the module start-up configuration.
- The IO port of the module is 3.3V. If the IO level of the main control and the module do not match, a level convert circuit needs to be added.
- If the I/O port is directly connected to a peripheral port or terminals, for example, a pin row, reserve an ESD device near the terminal of the I/O cable.

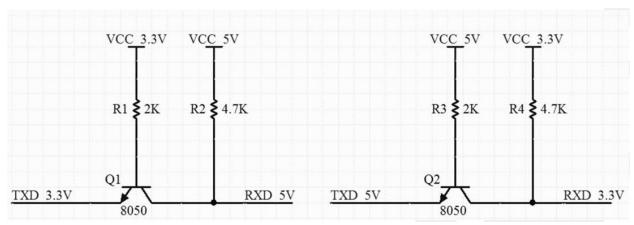


Figure 15 Level convert circuit



8. Storage conditions

Products sealed in moisture-proof bags should be stored in a non-condensing atmosphere of <40 ° C /90%RH.

The module has a moisture sensitivity rating of MSL 3.

After the vacuum bag is opened, it must be used within 168 hours at 25±5°C/60%RH, otherwise it needs to be baked before it can be put on line again.

9. Reflow welding curve diagram

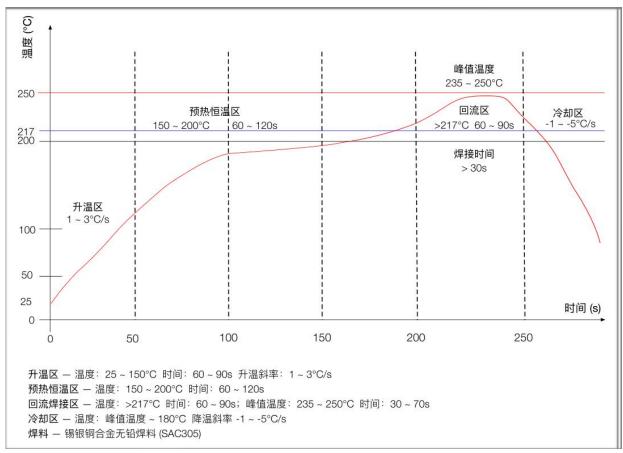


Figure 16 Reflow welding diagram



10. Product Packaging Information

Ai-WB2-01N module was packaged in a tape, 1000pcs/reel. As shown in the below image:



Figure 17 Package and packing diagram

11.Contact us

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